

This Page Is Inserted by IFW Operations
and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

IMAGES ARE BEST AVAILABLE COPY.

**As rescanning documents *will not* correct images,
please do not report the images to the
Image Problem Mailbox.**



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Guy B. Irving et al.
Serial No.: 09/848,816
Filing Date: May 4, 2001
Group Art Unit: 2111
Examiner: Khanh Nmn Dang
Title: **SERVER CHASSIS HARDWARE MASTER
SYSTEM AND METHOD**

MAIL STOP AMENDMENT

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

DECLARATION PURSUANT TO 37 C.F.R. § 1.131

I, the undersigned, hereby declare and state that:

1. I am over the age of 21 years, of sound mind, and competent in all respects to make this Declaration.

2. I am an inventor of the subject matter of the above-referenced patent application, entitled *Server Chassis Hardware Master System and Method*, filed on May 4, 2001 (the "Application").

3. Guy B. Irving and I (the "Inventors") conceived the subject matter of at least Claims 1, 9, 10, 13, and 16 of the Application (the "Invention") prior to July 12, 2000.

4. I participated in the design and construction of a prototype that incorporated the subject matter of the Invention. Attached as Exhibit A is the sixth version of a design schematic that was used to construct the prototype.

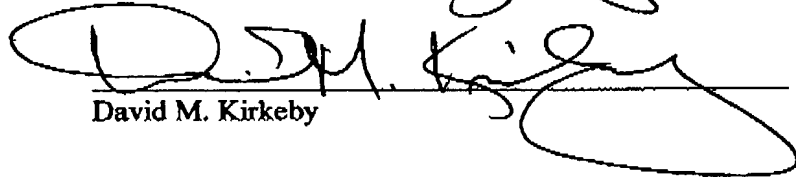
5. During a time period beginning prior to July 12, 2000, and ending upon completion of the prototype, the Inventors worked diligently toward the design and construction of the prototype.

6. I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true. Further, I declare that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the Application or any patent issuing thereon.

[THE REST OF THE PAGE INTENTIONALLY LEFT BLANK]

Declaration pursuant to 37 C.F.R. § 1.131 in regard to 09/848,816.

Signed this 19 day of July, 2004.


David M. Kirkeby

Page# Page Title

1	Index
2	Slot 1 & 2 SCA-2 Connectors
3	Slot 3 & 4 SCA-2 Connectors
4	Slot 5 & 6 SCA-2 Connectors
5	Slot 7 & 8 SCA-2 Connectors
6	Slot 9 & 10 SCA-2 Connectors
7	Slot 11 & 12 SCA-2 Connectors
8	Slot 13 & 14 (DEBUG) SCA-2 Connectors
9	Slot 15 & 16 SCA-2 Connectors
10	Slot 17 & 18 SCA-2 Connectors
11	Slot 19 & 20 SCA-2 Connectors
12	Slot 21 & 22 SCA-2 Connectors
13	Slot 23 & 24 SCA-2 Connectors
14	RLXRJ21 Connectors - Public
15	RLXRJ21 Connectors - Private
16	RLXHUB Connectors
17	Power Supply Connector 1
18	Power Supply Connector 2
19	RLXLED Conn., ATX Conn., Pull-Ups, Decoupling
20	Fiducials, Holes, Spare Gates

Released Rev. A.6 (000723.2000)



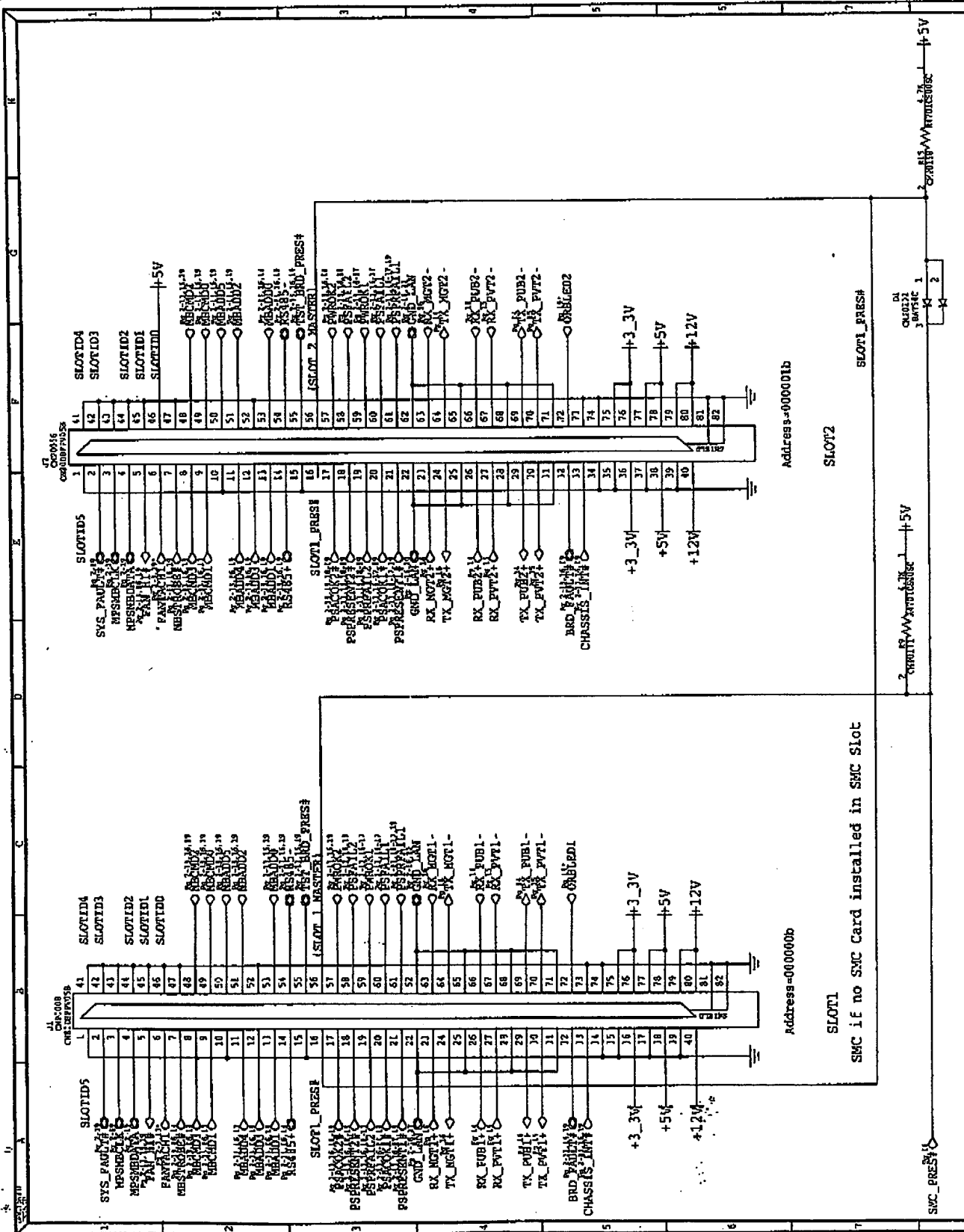
Anigma
Inc.

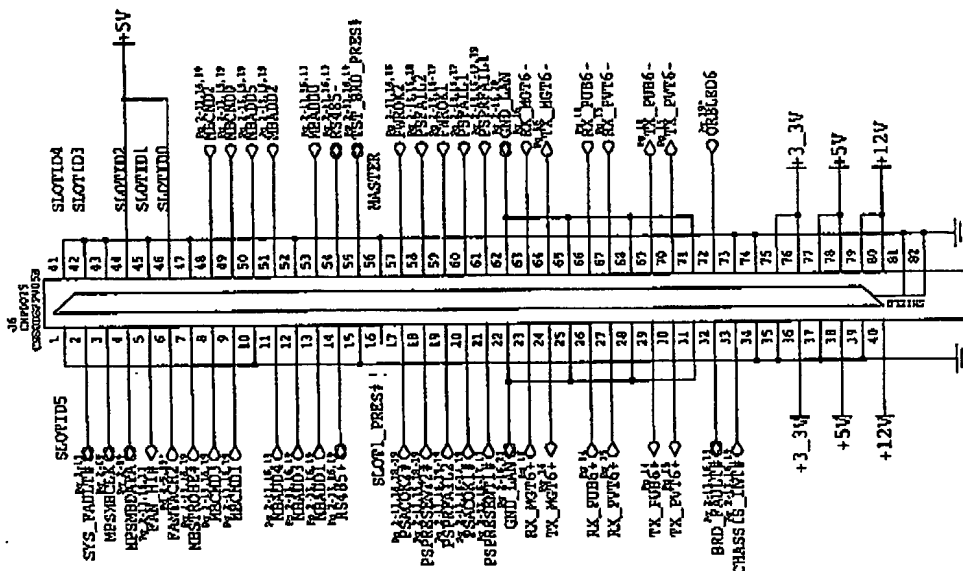
Title: RLXHP

Drawing Number: RLXHP-PCB-01-SC Revision: A.6

Release Date: July 23, 2000

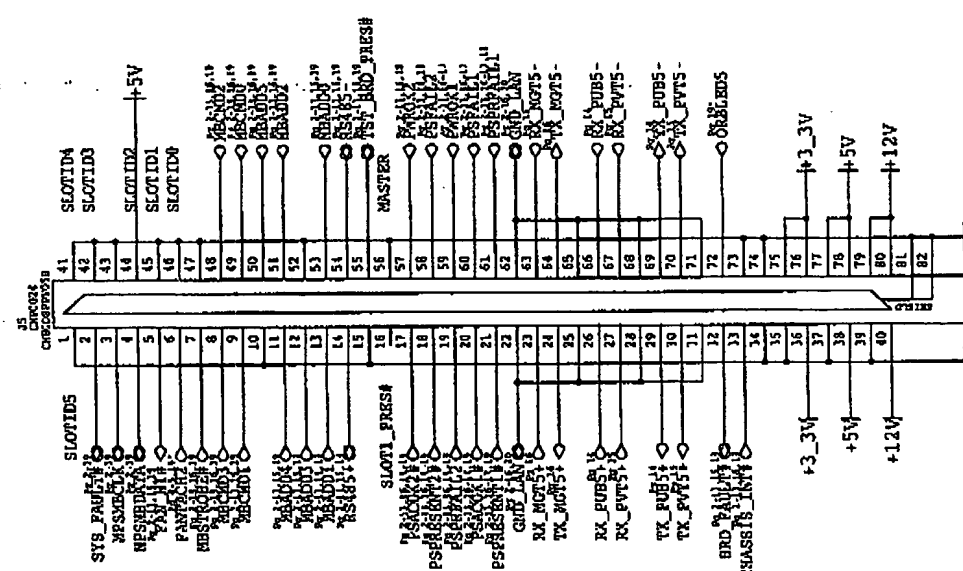
Sheet 1 of 20





Address=000101b

SLOT5

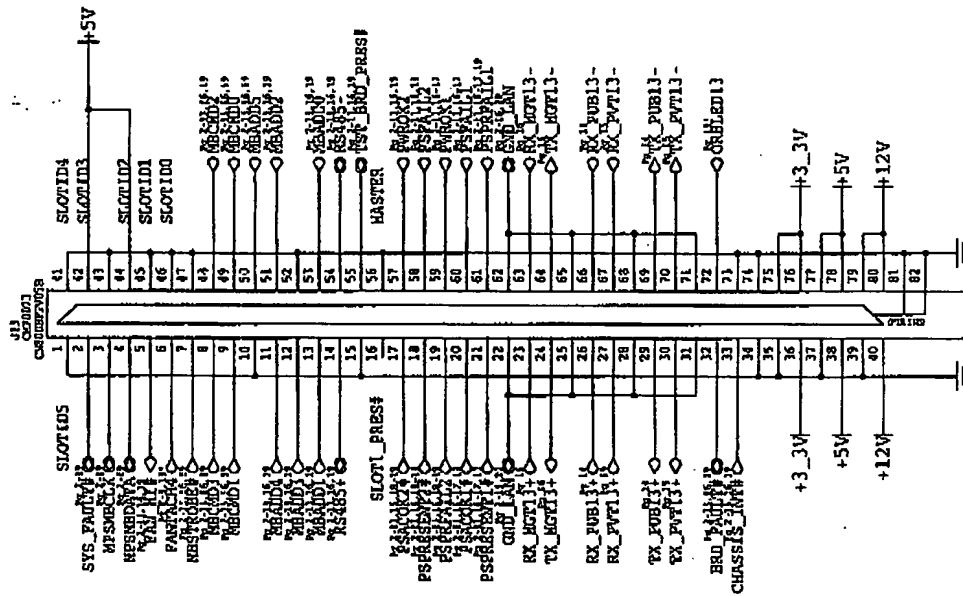


Address=000100b

SLOT6

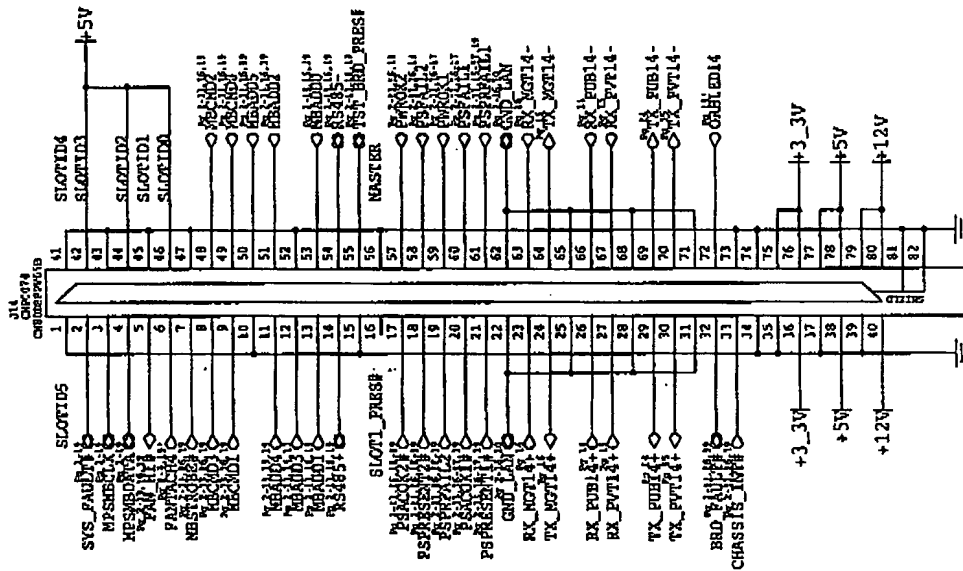






Address=001100b

SLOT13



Address=001101b

SLOT14
DEBUG SLOT



Anigma
Inc.

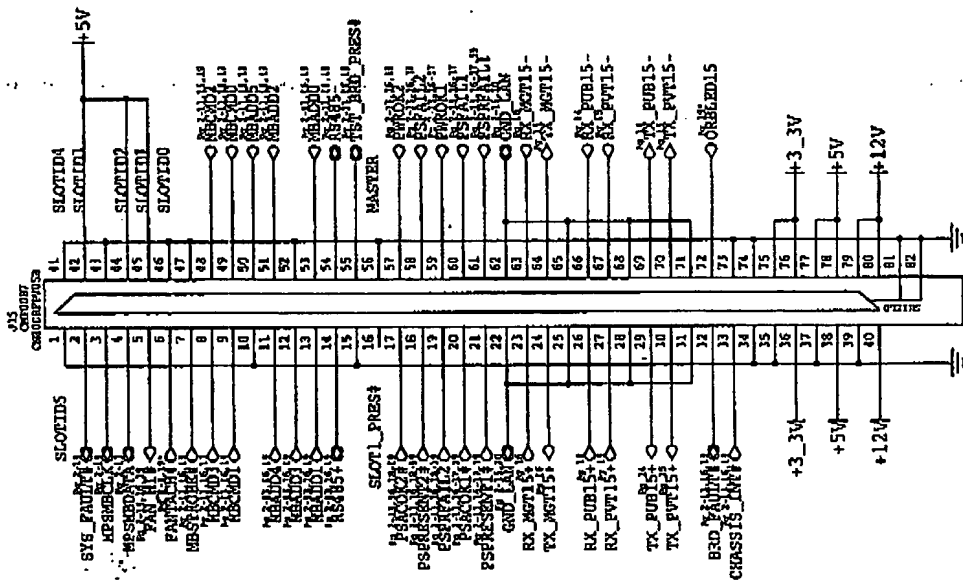
Title: RLXMP

Drawing Number: RLXMP-PCB-01-SC-Revision: A.6

Release Date: July 23, 2000

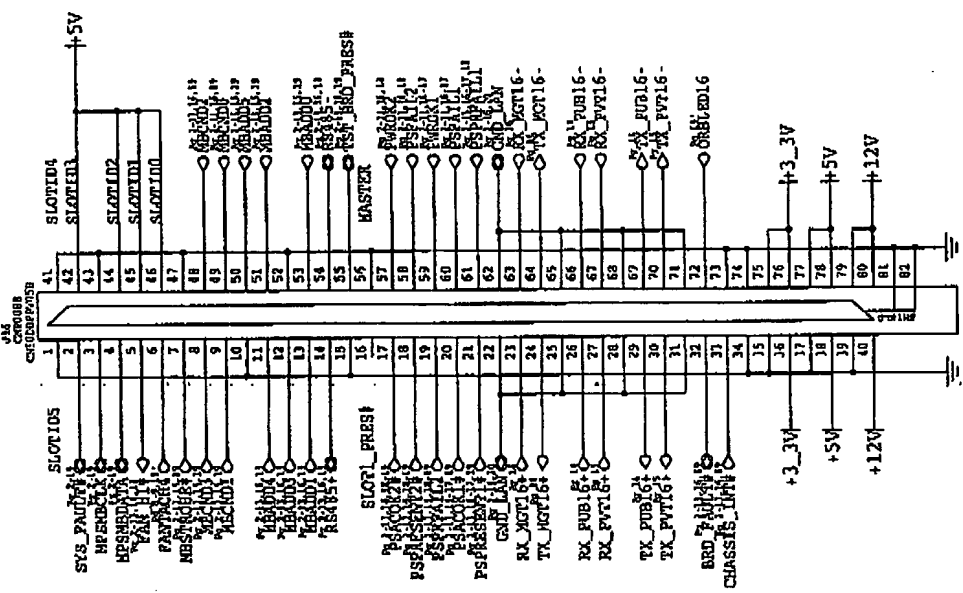
Page Title: Slot 13 & 14 (DEBUG) SCA-2 Connectors

Sheet 8 of 20



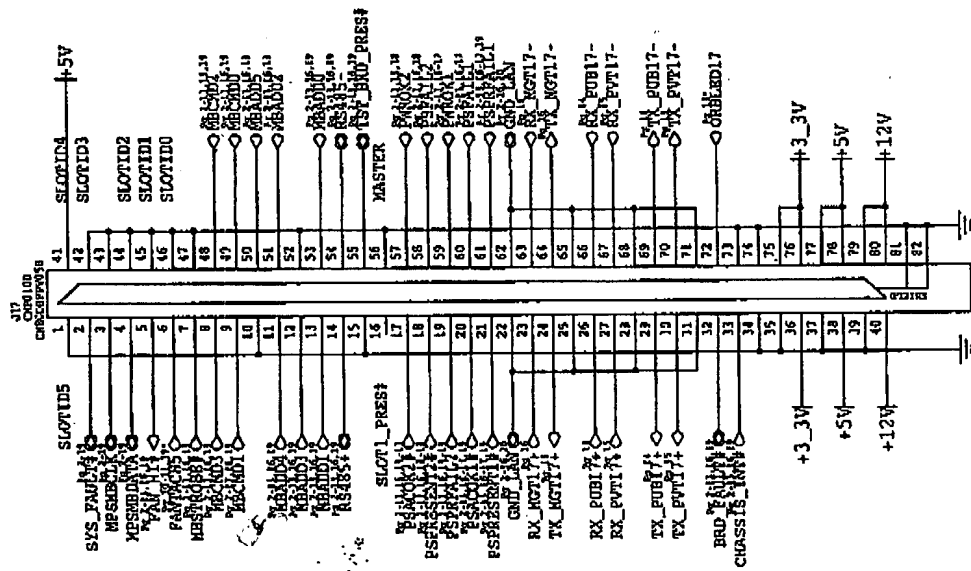
Address=001110b

SLOT15



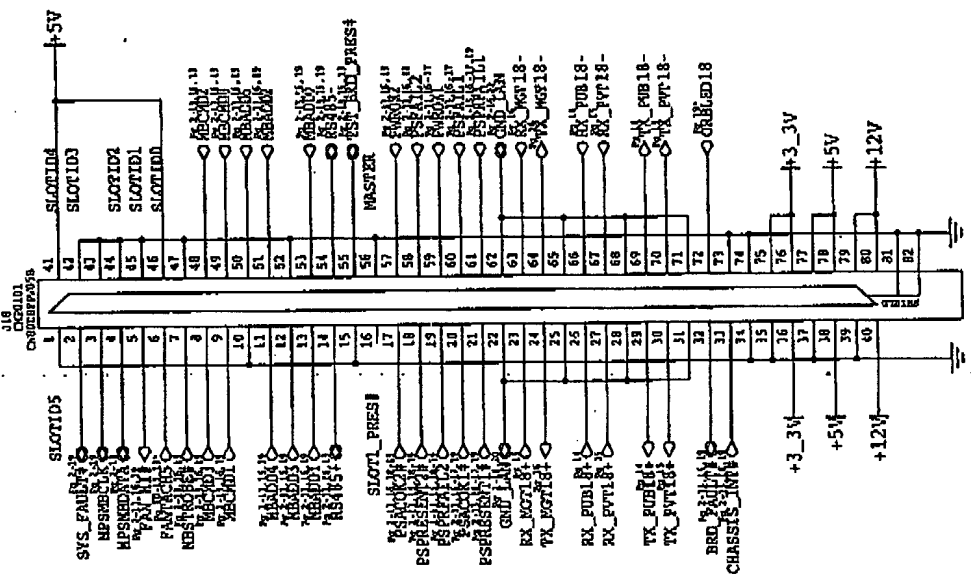
Address=001111b

SLOT16



Address=010000b

SLOTT17



Address=010001b

SLOTT18

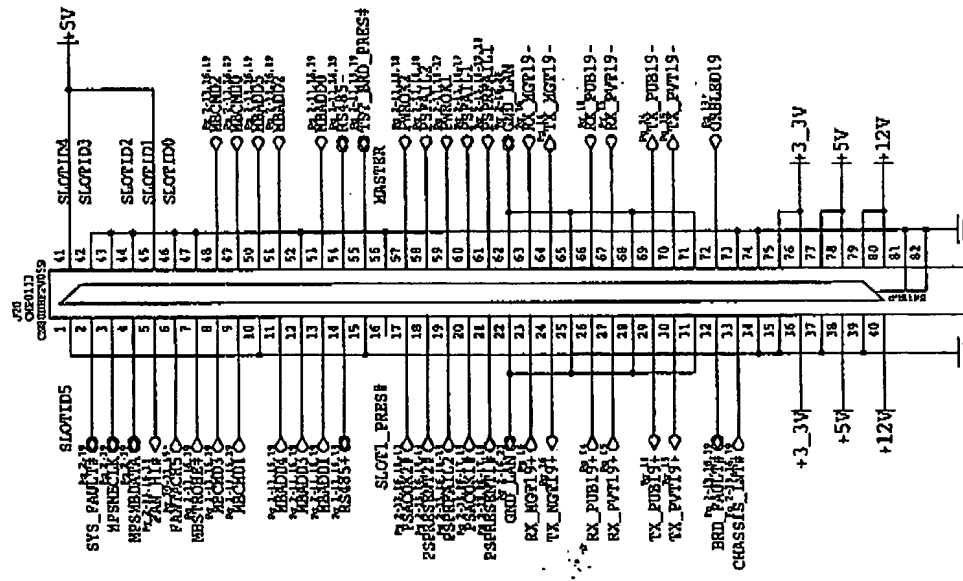


Anigma
Inc.

Title: RLXNP

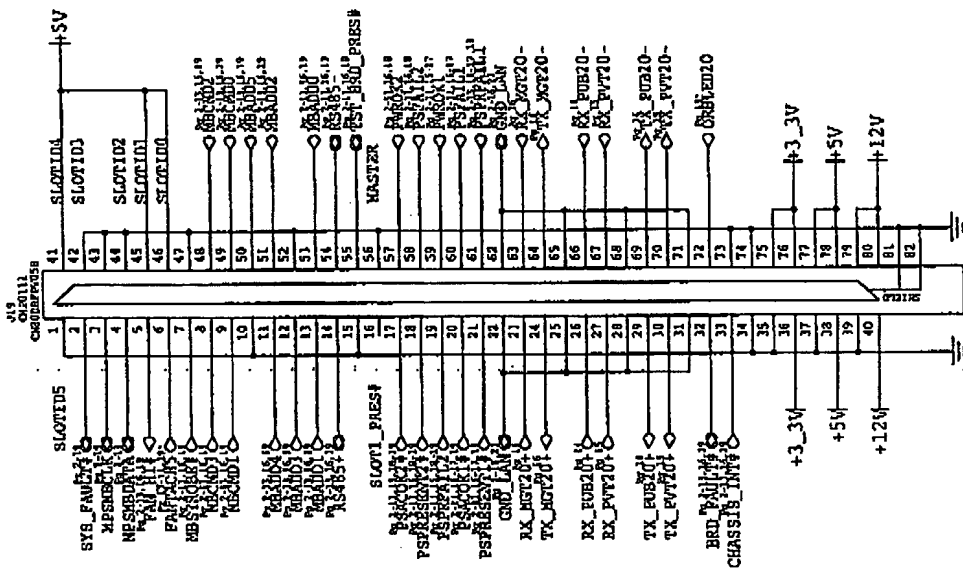
Drawing Number: RLXNP-PCB-01-SC/Revision: A.6

Release Date: July 23, 2000 Sheet 10 of 20



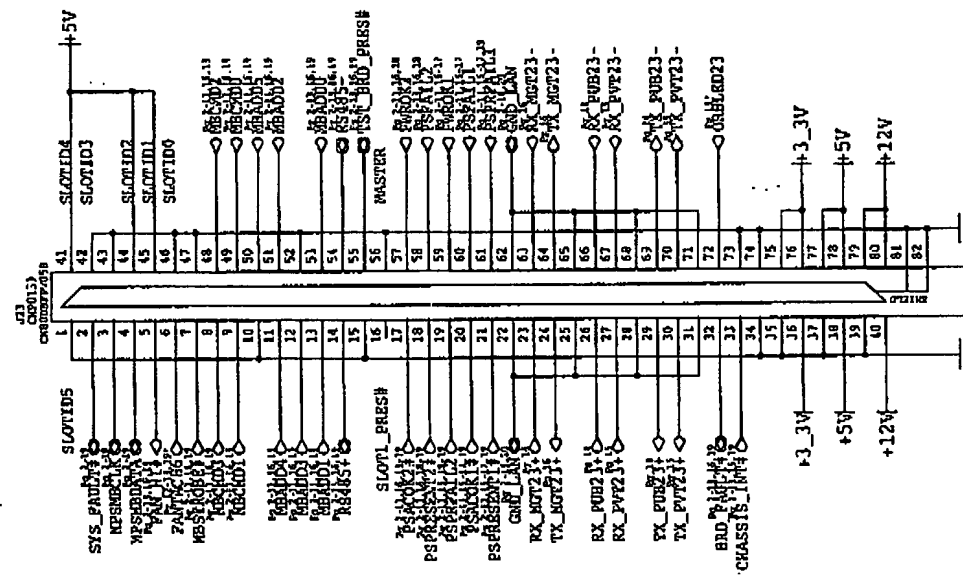
Address=010010b

SLOT19



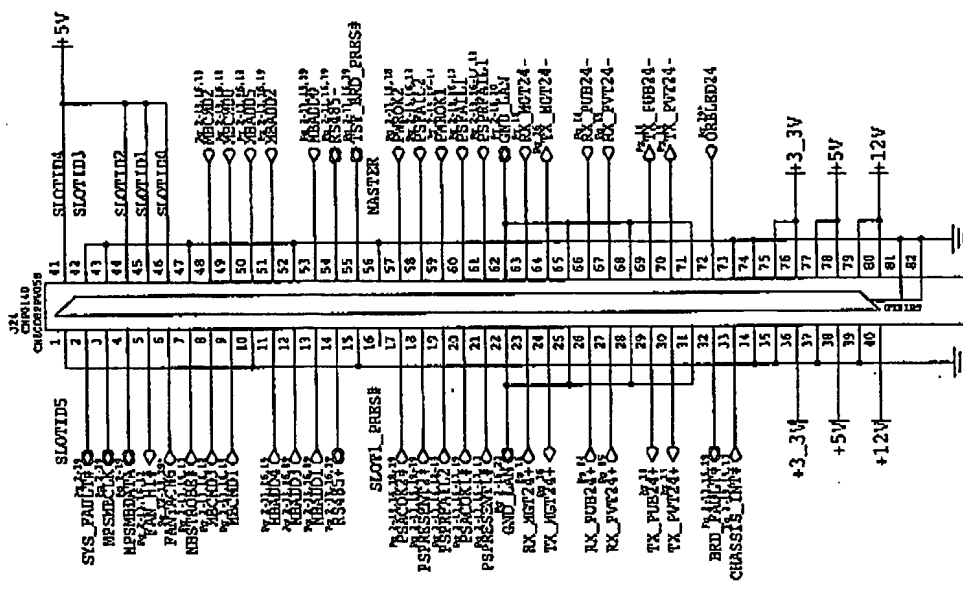
Address=010011b

SLOT20



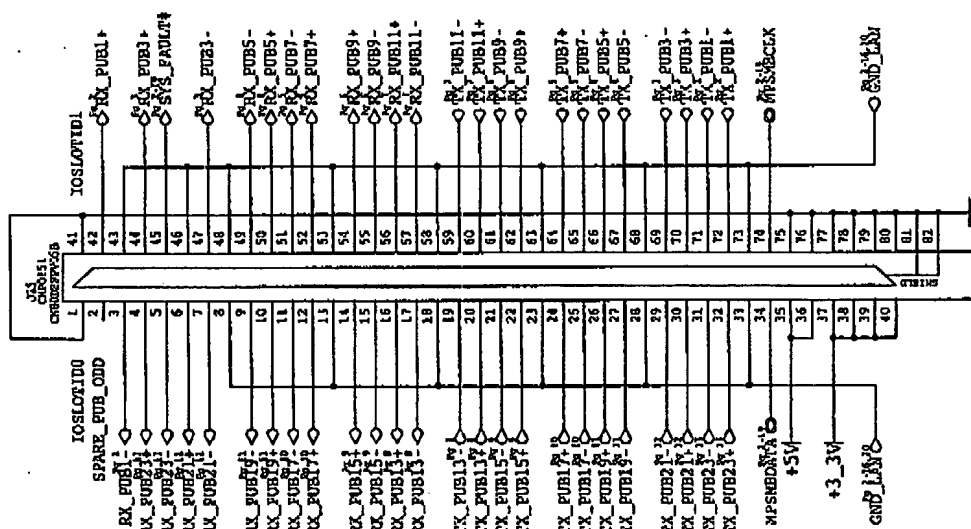
Address=010110b

SLOT23



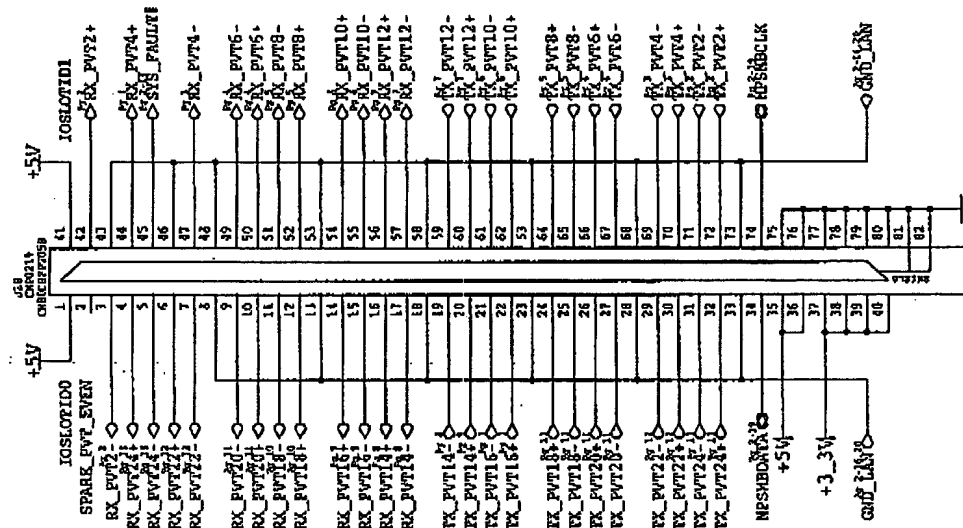
Address=010111b

SLOT24

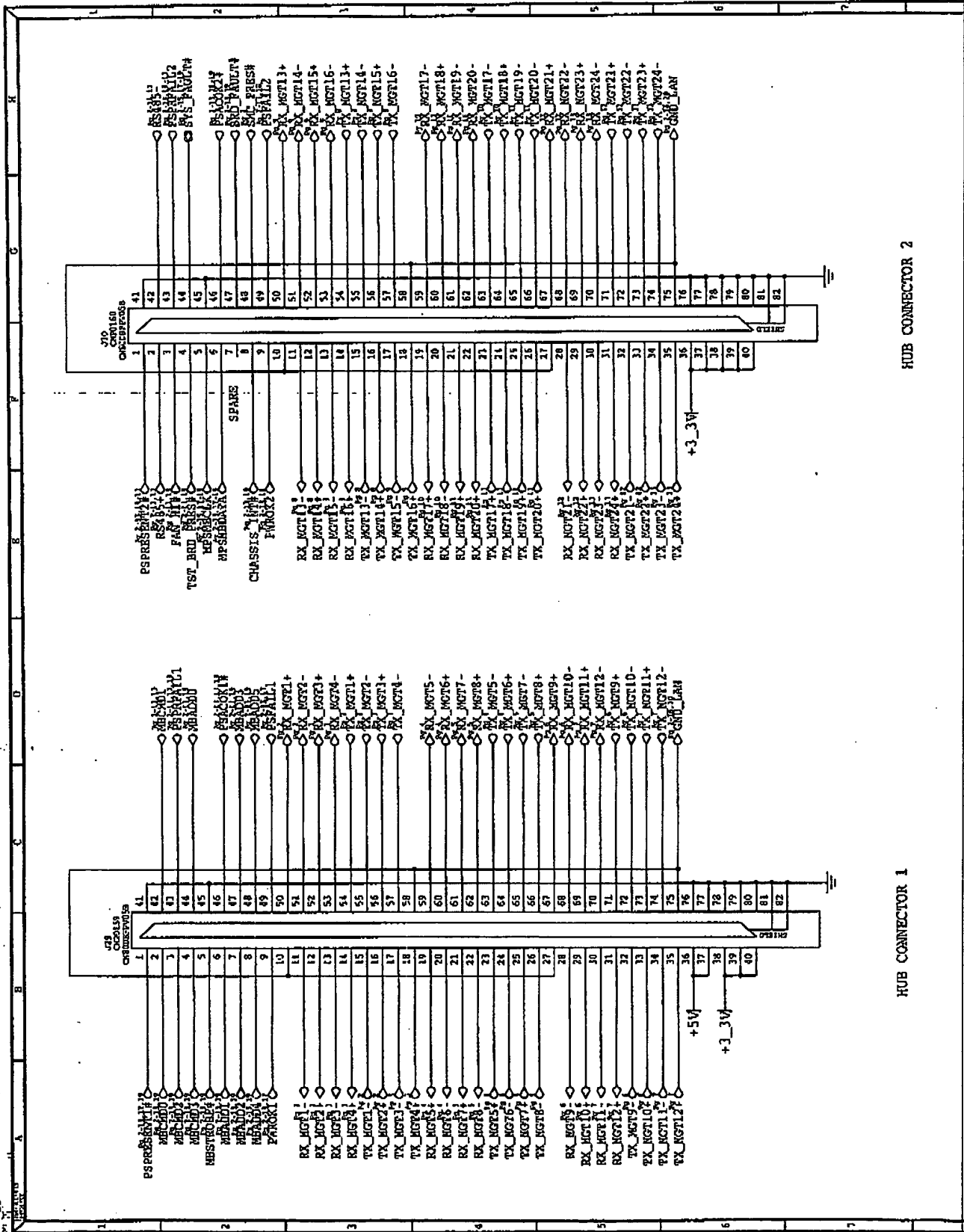


Address=01b

Address=00b

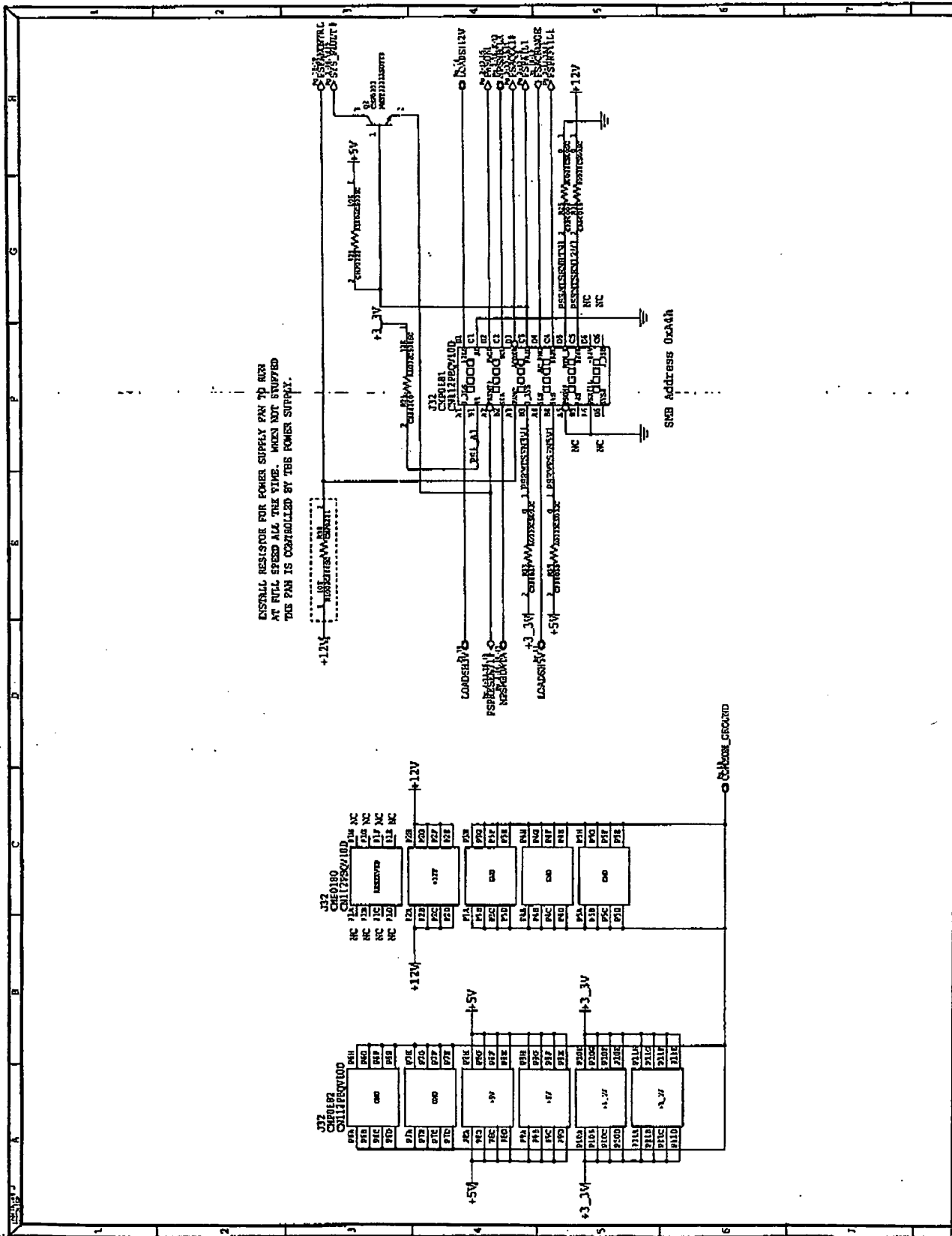


PRIVATE LAN - EVEN SLOTS
RLXRJ21 SLOT 4
Address=11b



HUB CONNECTOR 1

HUB CONNECTOR 2



INSTALL RESISTOR FOR POWER SUPPLY FAN TO RUN
AT FULL SPEED ALL THE TIME. WHEN NOT SUPPLIED
THE FAN IS CONTROLLED BY THE POWER SUPPLY.



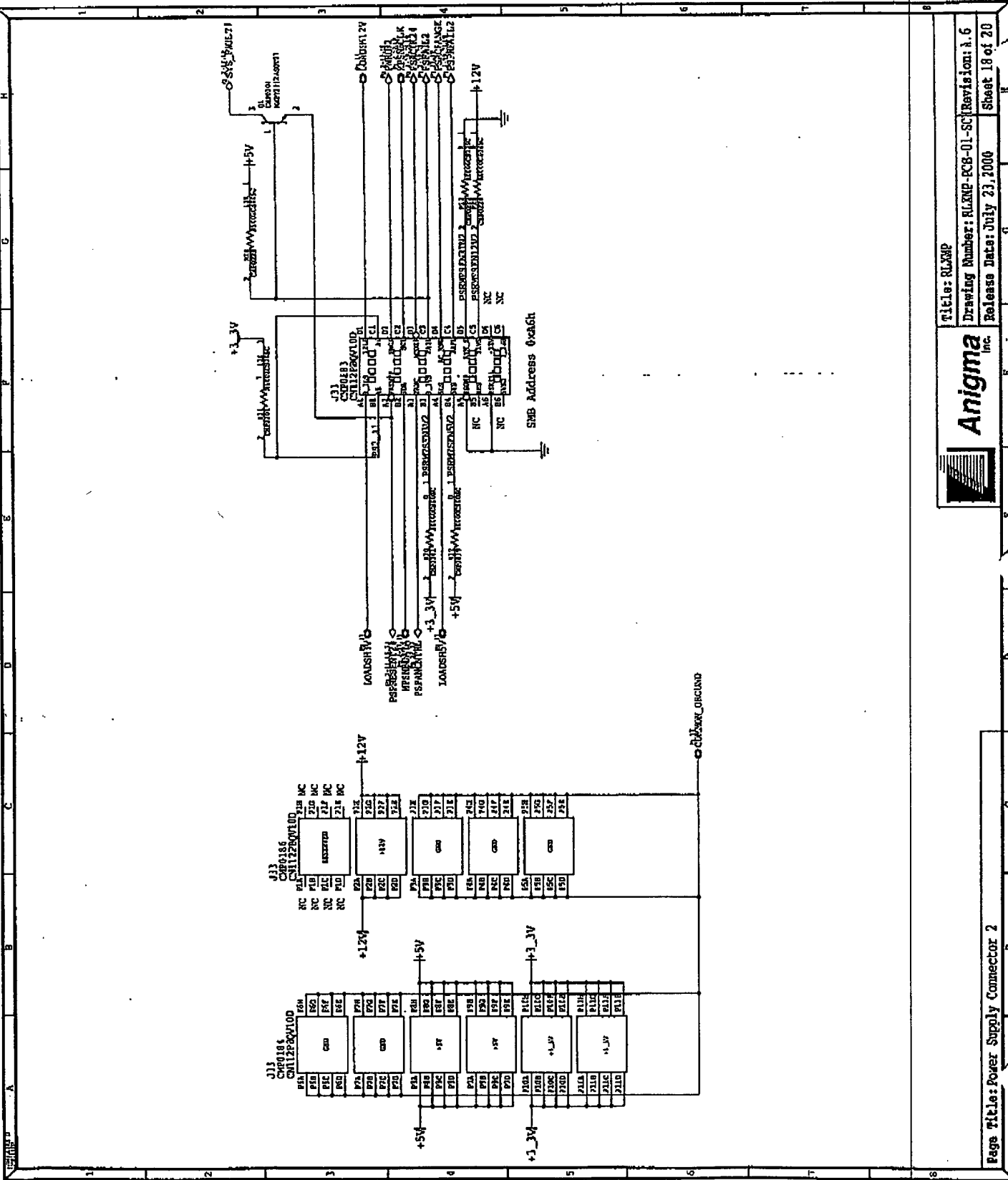
Anigma
INC.

File: RLXNP

Drawing Number: RLXNP-PCB-01-SC-Revision A.6

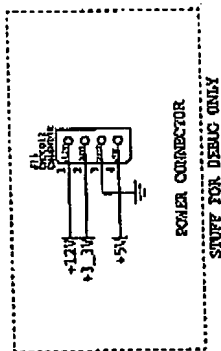
Release Date: July 23, 2000

Sheet 17 of 20



Title: RLXMP
 Drawing Number: RLXMP-PCB-01-SC (Revision: 1.6)
 Release Date: July 23, 2000
 Sheet 18 of 20

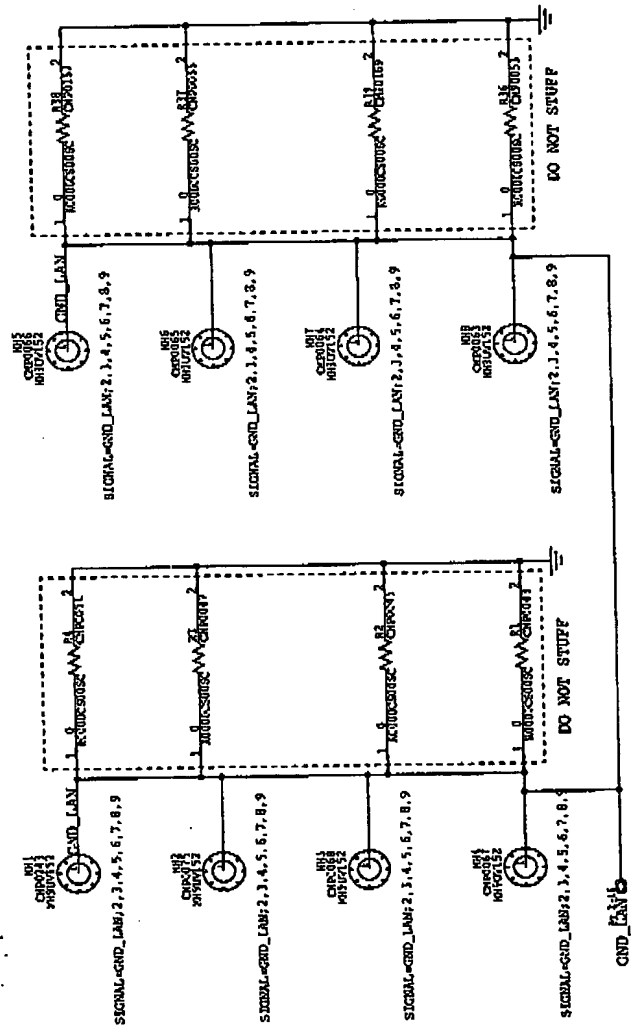


[illegible]

Fiducial Marks



Mounting Holes



Title: REXMP
 Drawing Number: REXMP-PCB-01-SC
 Revision: A.6
 Release Date: July 23, 2000
 Sheet 20 of 20

